

Amendment and Response

Applicant: Robert Walker

Serial No.: 10/730,443

Filed: December 8, 2003

Docket No.: I331.124.101/2003P54210US

Title: CHIP TO CHIP INTERFACE

REMARKS

The following remarks are made in response to the Office Action mailed June 2, 2005. Claims 1-14 and 27-31 have been allowed. Claim 24 was rejected. Claims 25 and 26 have been objected to. With this Response, claims 25 and 26 have been amended. Claims 1-14 and 24-31 remain pending in the application and are presented for reconsideration and allowance.

In the Specification

The title of the invention was objected to for not being descriptive. The title of the invention has been amended to "CHIP TO CHIP INTERFACE FOR ENCODING DATA AND CLOCK SIGNALS." Accordingly, the objection to the title has been traversed.

Claim Rejections under 35 U.S.C. § 102

The Examiner rejected claim 24 under 35 U.S.C. § 102(e) as being anticipated by Eccles U.S. Patent No. 6,574,758 ("Eccles"). Applicant submits that Eccles fails to teach or suggest the invention of independent claim 24.

Eccles fails to teach or suggest **means for decoding the encoded signal to recreate the clock signal**.

Eccles discloses in Figure 4 and col. 4, lines 53-67 and col. 5, lines 1-47, a test signal generator 20 included in circuit 16 that is coupled to a storage element (also called "pattern register") 21 to receive therefrom a predetermined pattern that is used to generate test signals. Test signal generator 20 generates and supplies to encoder 17 two test signals in each cycle, thereby to test the use of successive clock edges (i.e. positive edge and negative edge) on bus 15. In the implementation illustrated in Figure 4, another bus 25 transfers data from circuit 18 to circuit 16, and coupled thereto are circuits (not labeled) of the type described above in reference to bus 15. Eccles does not teach or suggest decoding the encoded signal to recreate the clock signal, where the encoded signal comprises a clock signal and a double data rate data signal.

In view of the above, Applicant submits that the above rejection of claim 24 under 35 U.S.C. § 102(e) should be withdrawn.

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Allowable Subject Matter

Claims 1-14 and 27-31 have been allowed.

Applicant thanks the Examiner for the indication that claims 25 and 26 would be allowable over the prior art if rewritten in independent form including all limitations of the base claim and any intervening claims. Claims 25 and 26 have been rewritten in independent form including all the limitations of the base claim and any intervening claims. Applicant agrees with the Examiner's conclusions regarding patentability without necessarily agreeing with or acquiescing in the Examiner's reasoning. In particular, applicant believes that the claims are allowable because prior art fails to teach, anticipate, or render obvious the invention as claimed, independent of how the invention is paraphrased.

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CONCLUSION

In view of the above, Applicant respectfully submits that pending claims 1-14 and 24-31 are in form for allowance and are not taught or suggested by the cited references. Therefore, reconsideration and withdrawal of the rejections and allowance of claims 1-14 and 24-31 is respectfully requested.

The Examiner is invited to contact the Applicant's representative at the below-listed telephone numbers to facilitate prosecution of this application.

Respectfully submitted,

Robert Walker,

By his attorneys,

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CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this paper or papers, as described herein, are being deposited in the United States Postal Service, as first class mail, in an envelope address to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 2nd day of September, 2005.

By 

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